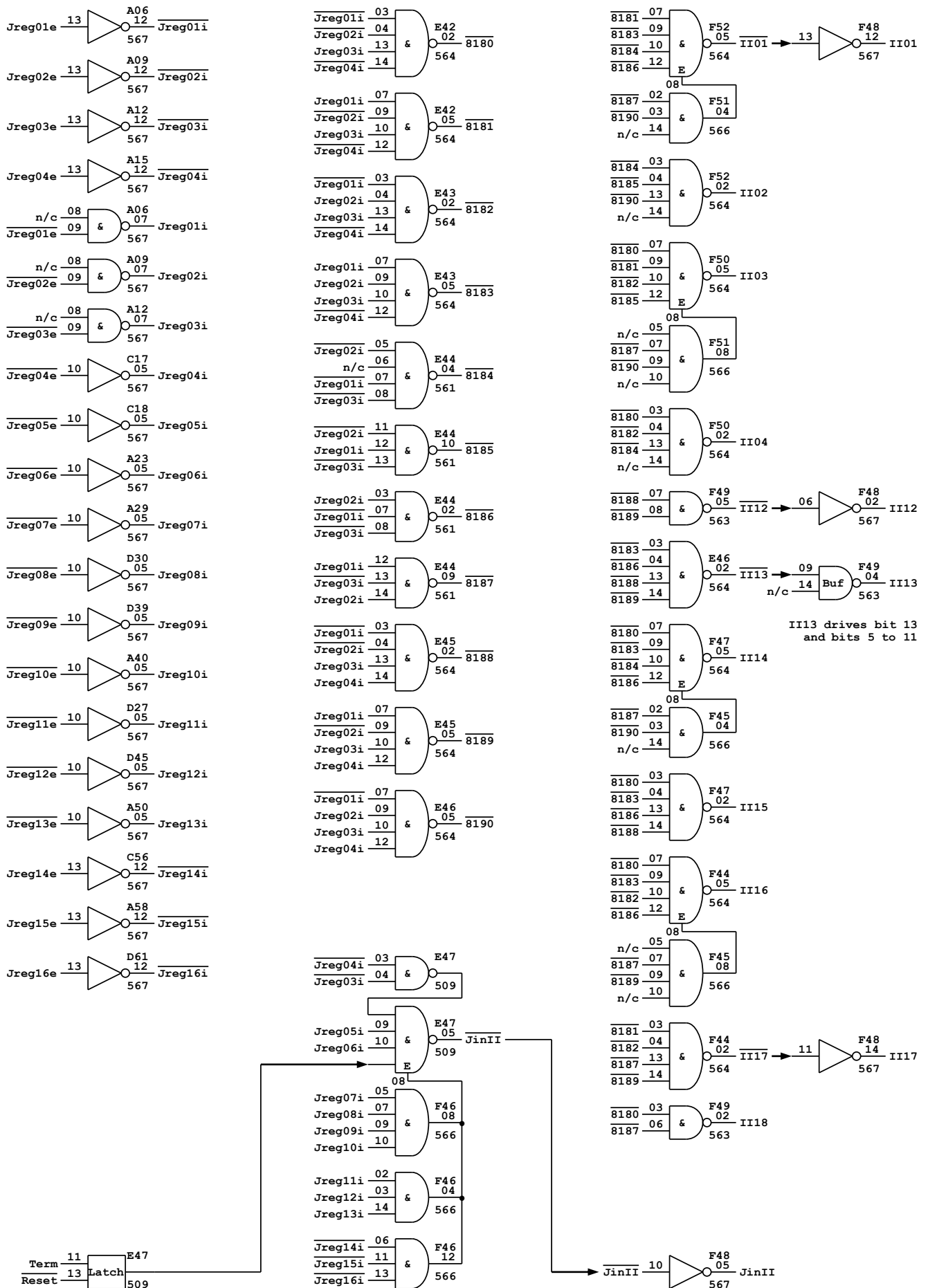


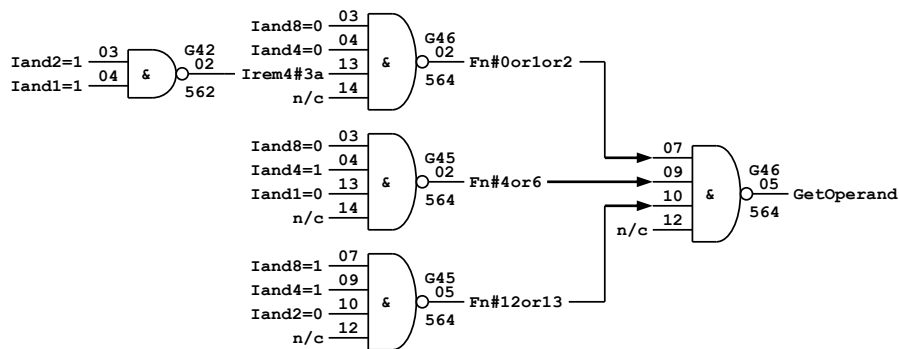
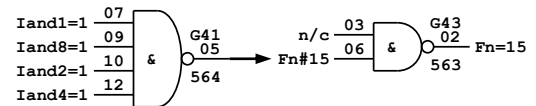
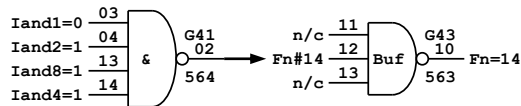
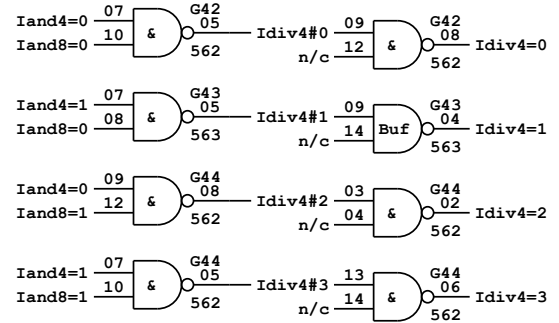
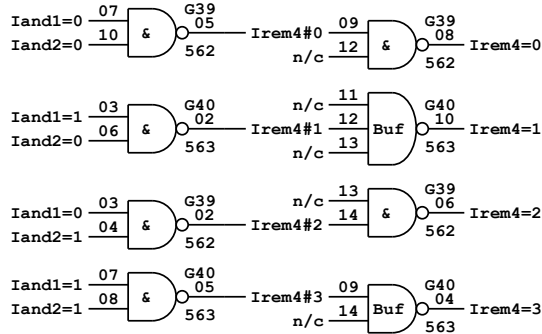
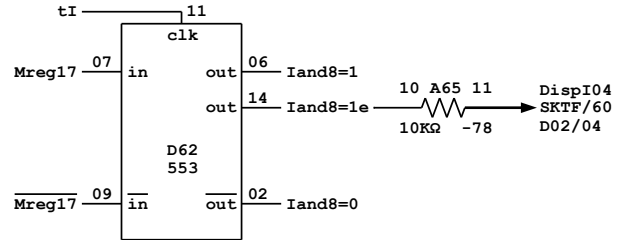
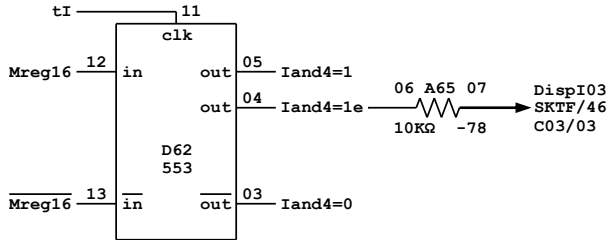
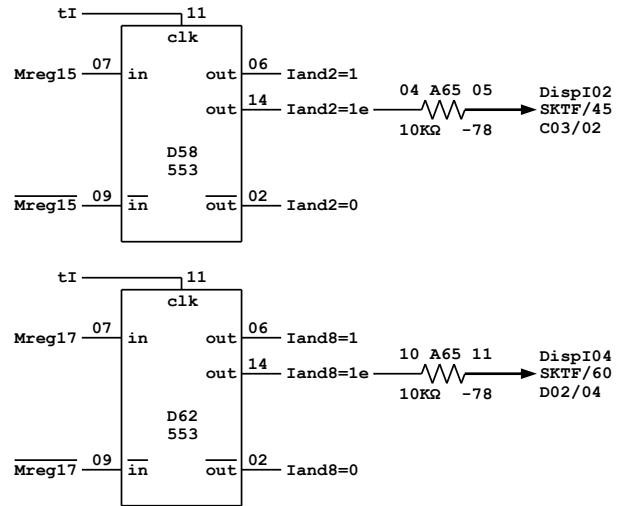
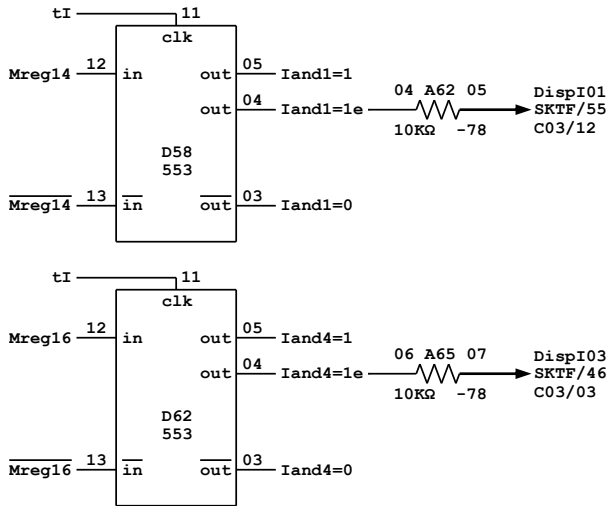
920M INITIAL INSTRUCTIONS

Terry Froggatt, October 2023



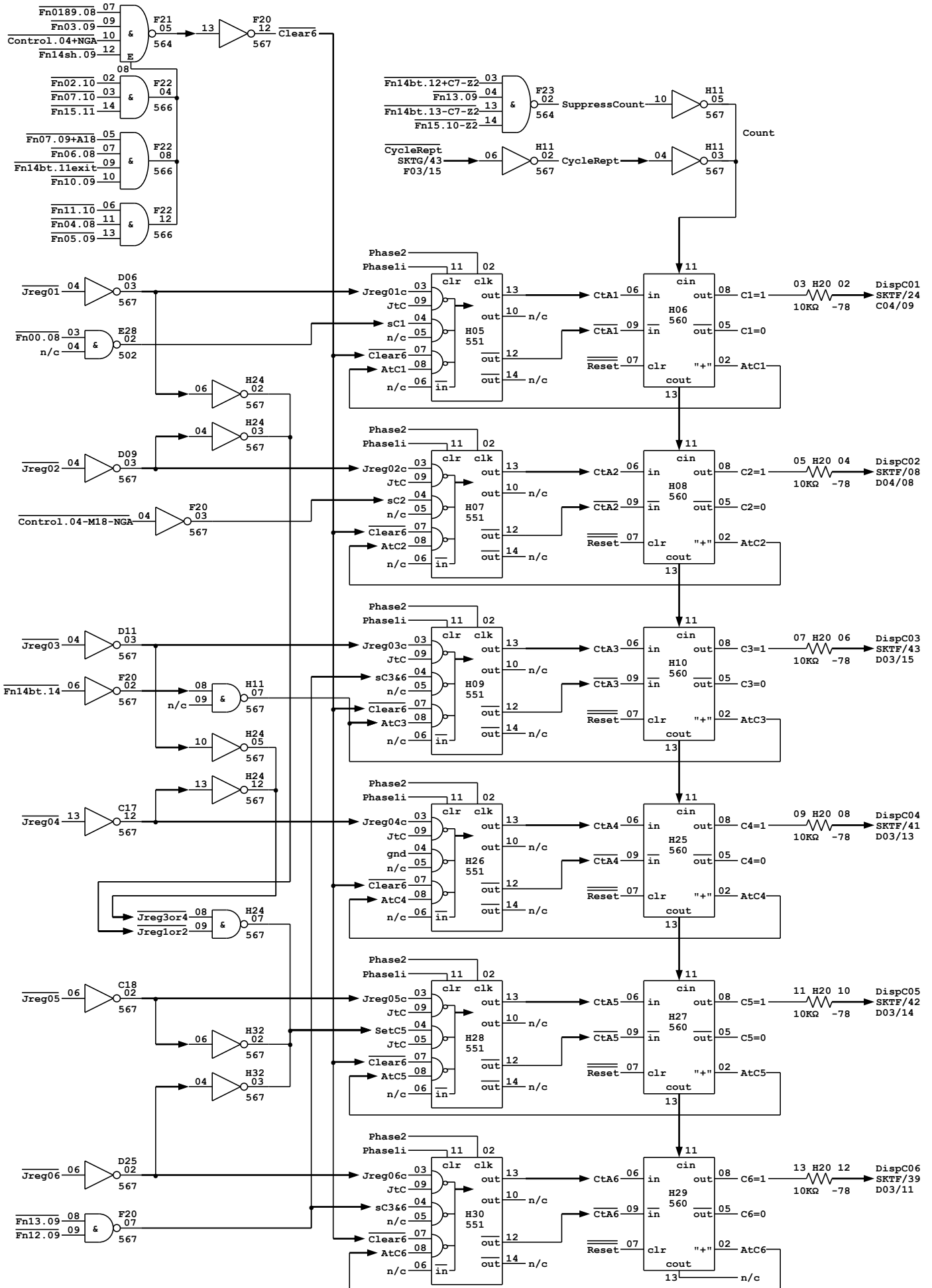
920M I-REGISTER & FUNCTION DECODE

Terry Froggatt, October 2023



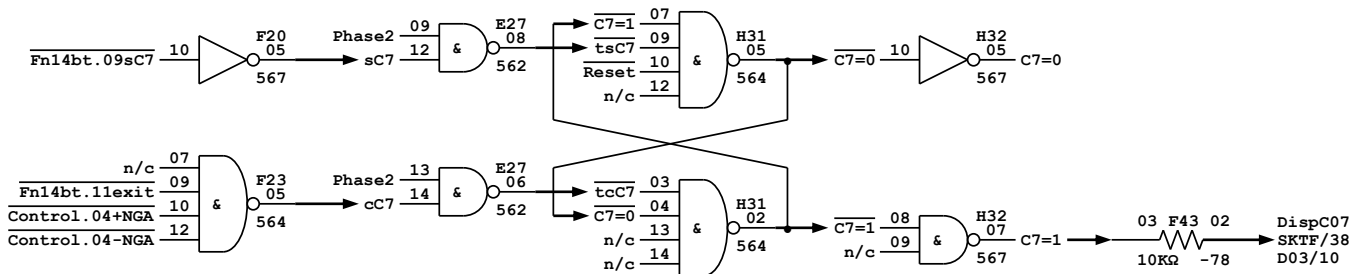
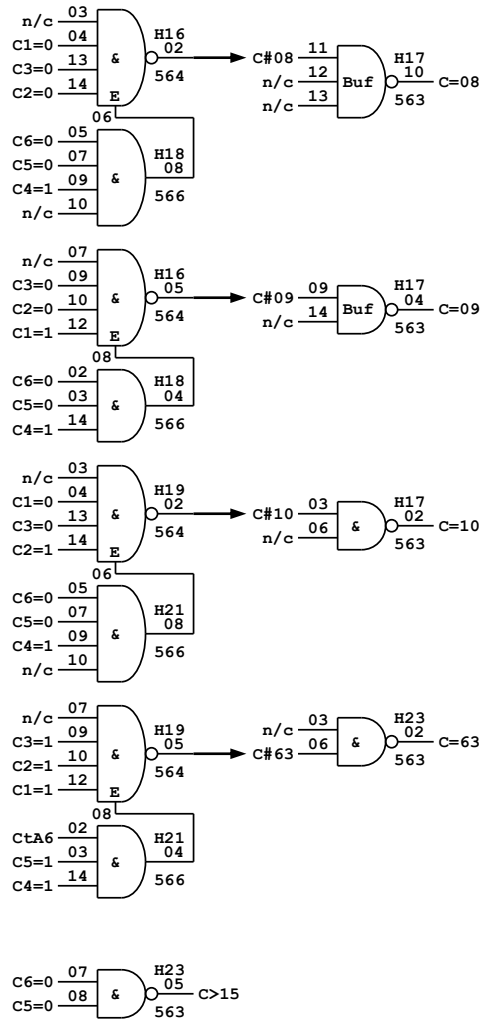
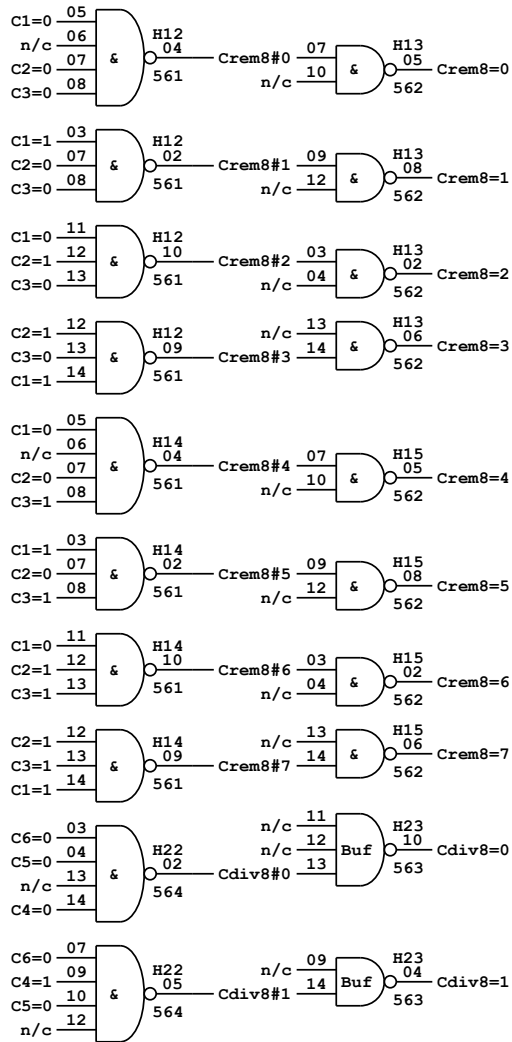
920M MICROPROGRAM COUNTER

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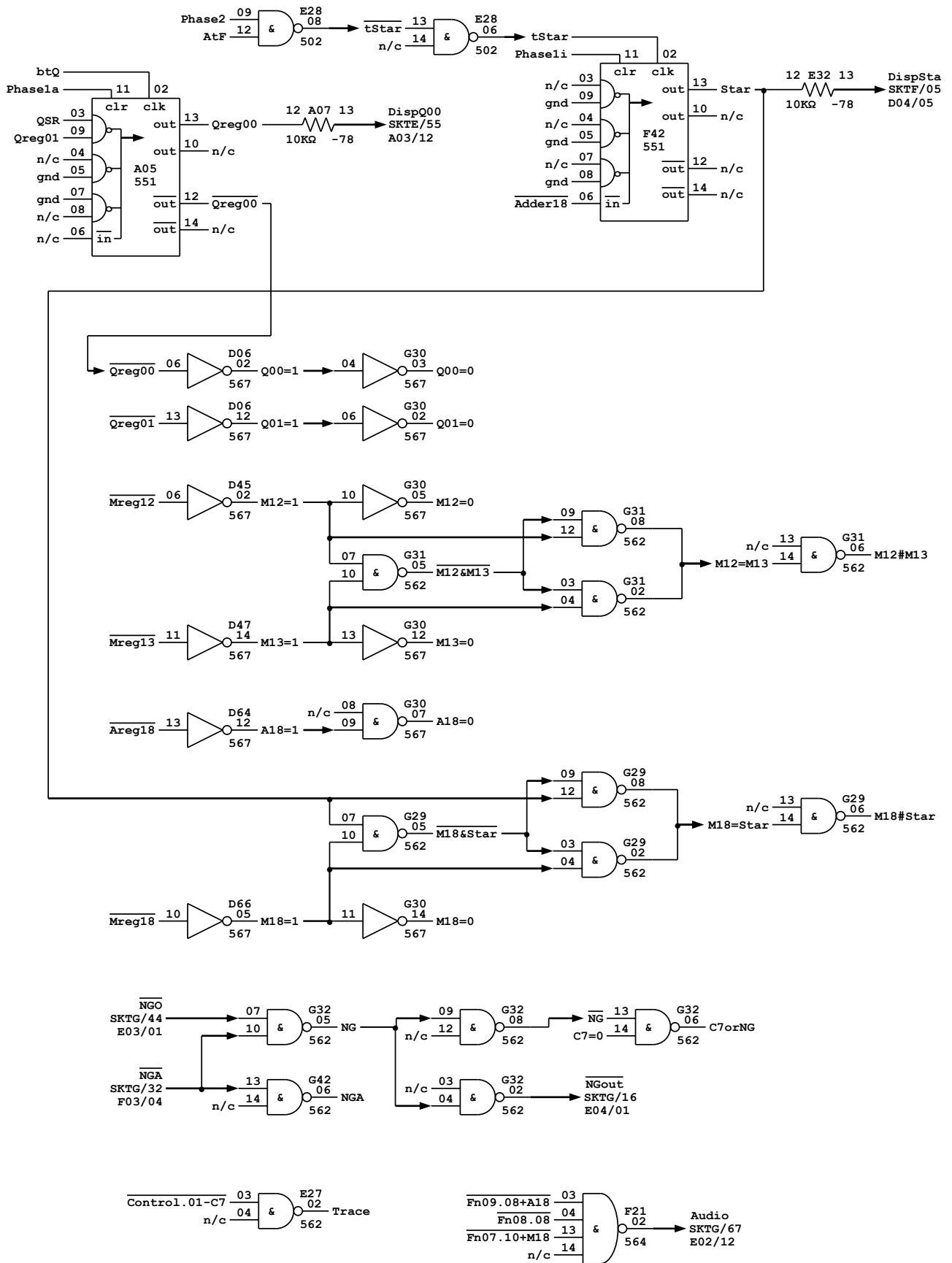
920M COUNTER DECODE & C7

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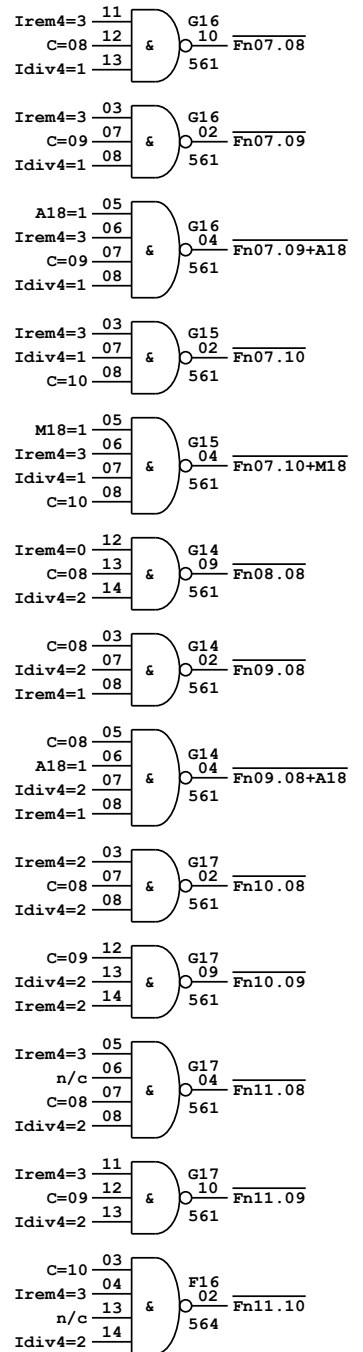
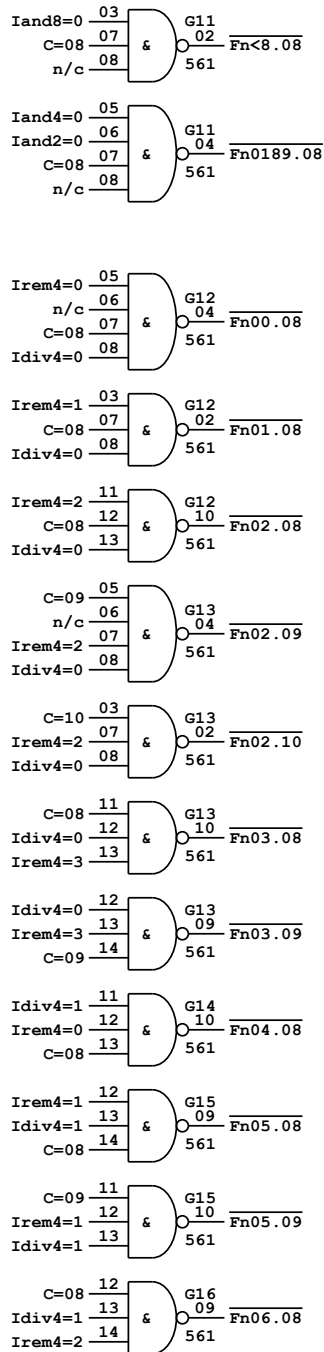
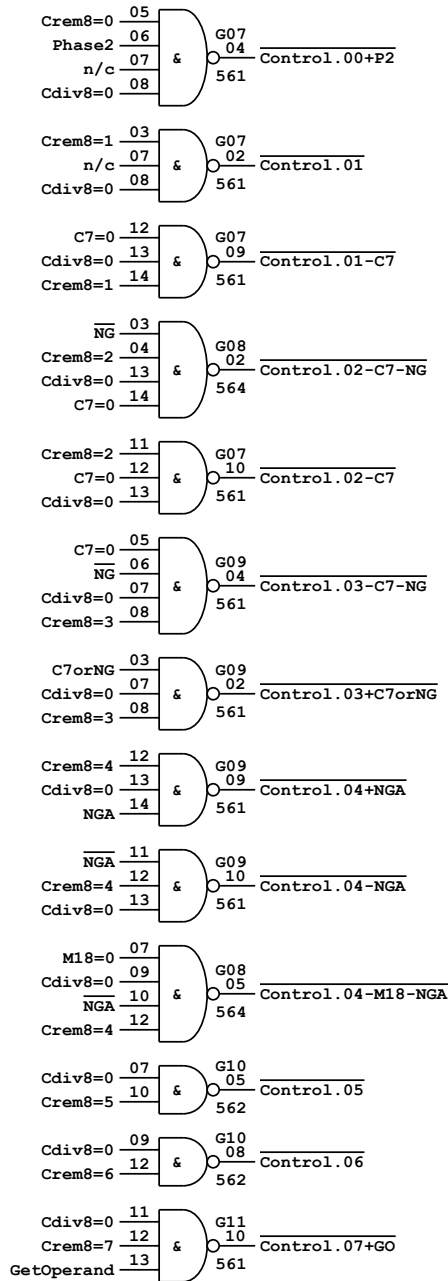
920M OTHER CONDITIONALS

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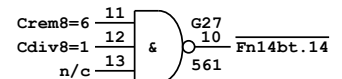
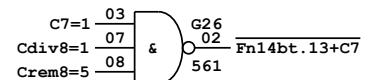
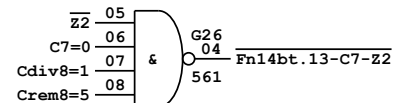
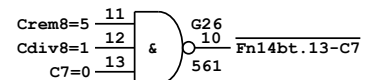
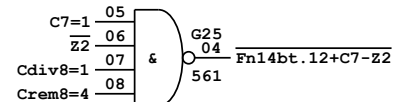
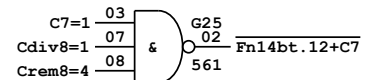
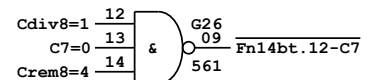
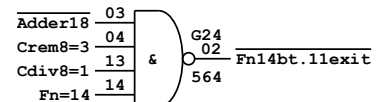
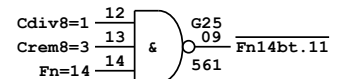
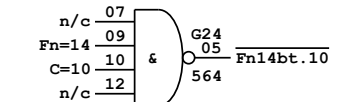
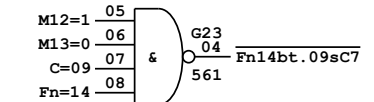
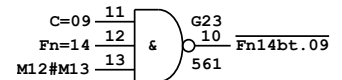
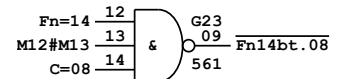
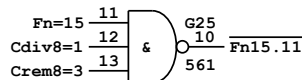
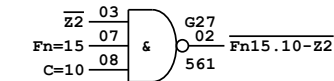
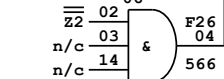
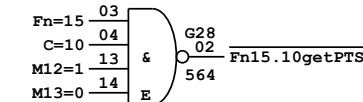
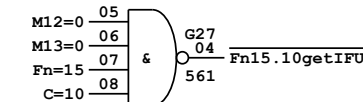
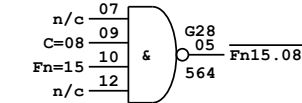
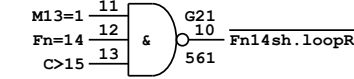
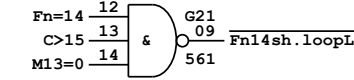
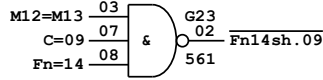
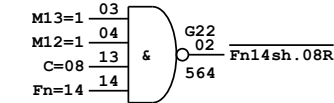
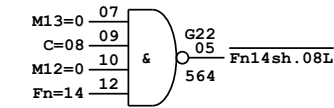
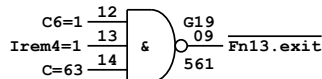
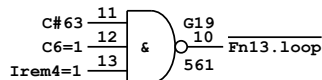
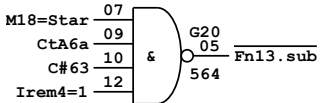
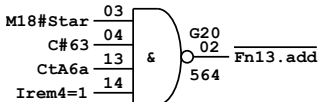
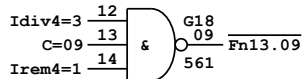
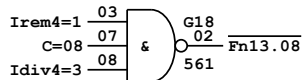
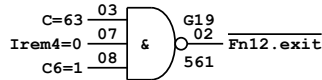
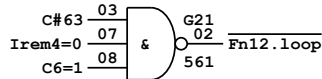
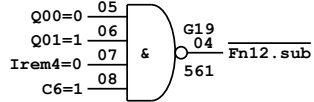
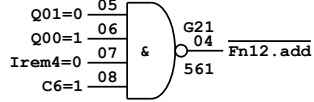
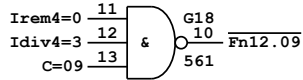
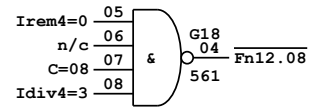
920M MICROPROGRAM DECODE 1of2

Terry Froggatt, October 2023



920M MICROPROGRAM DECODE 2 of 2

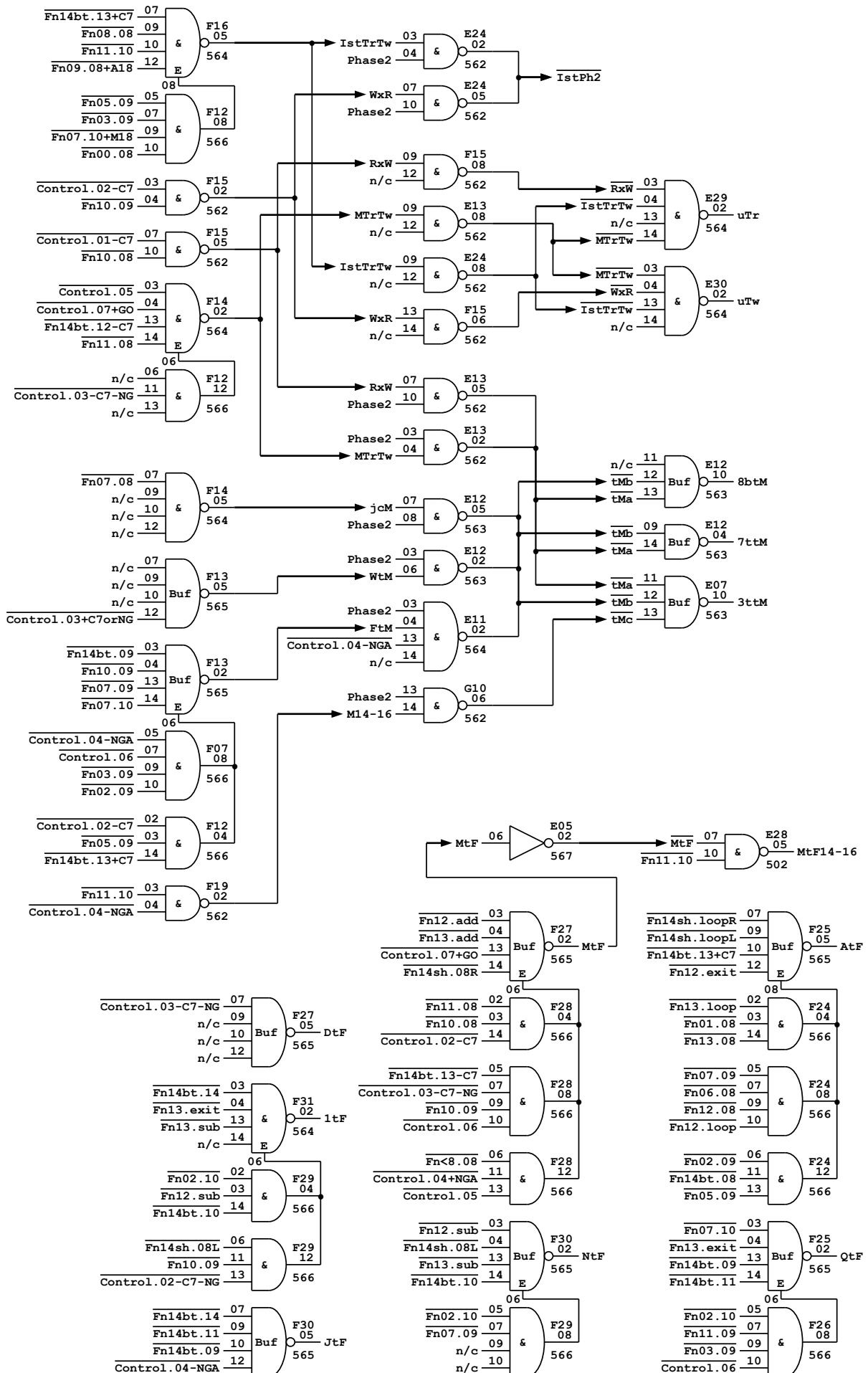
Terry Froggatt, October 2023



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920M GATING SIGNALS 2 of 2

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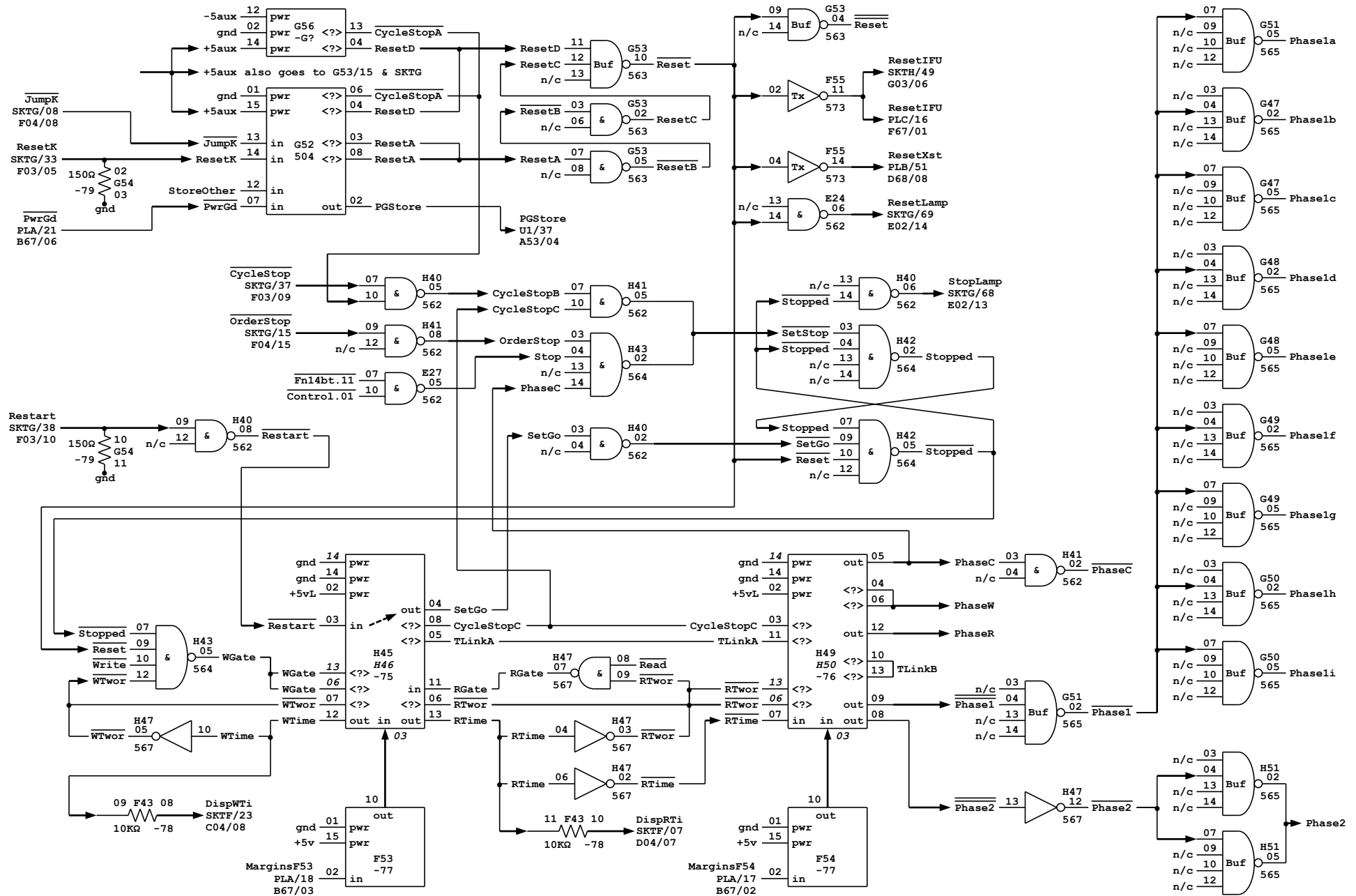


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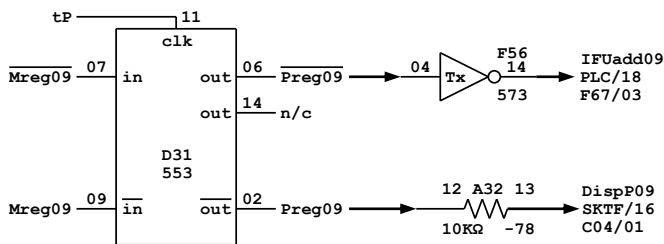
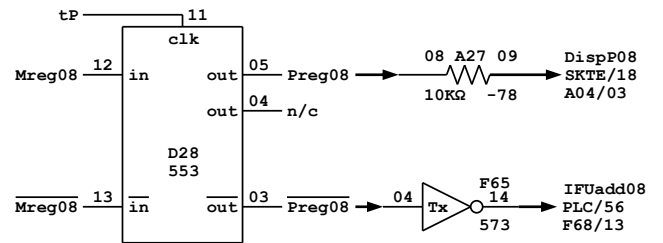
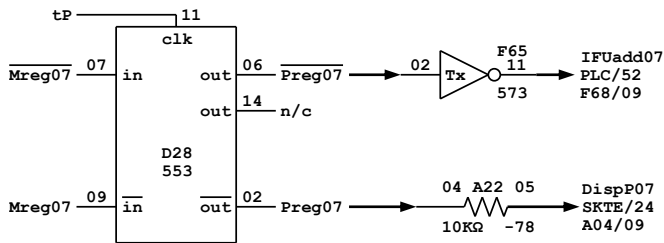
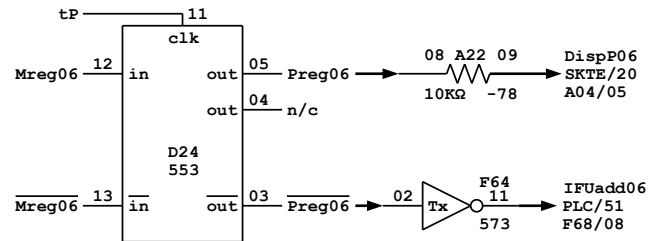
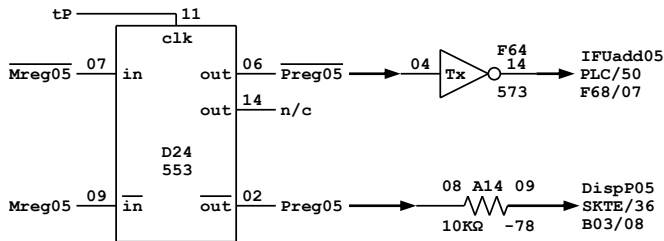
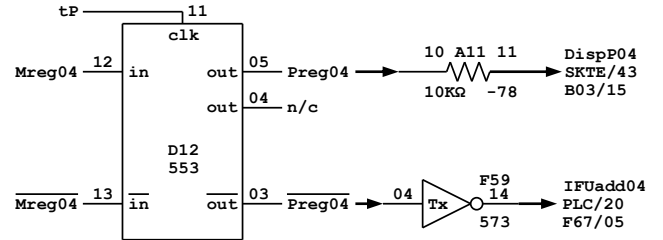
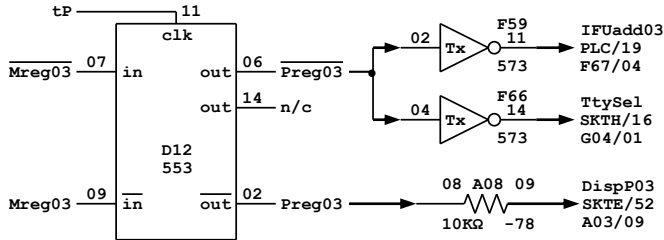
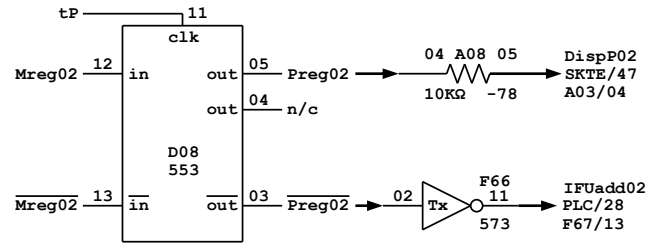
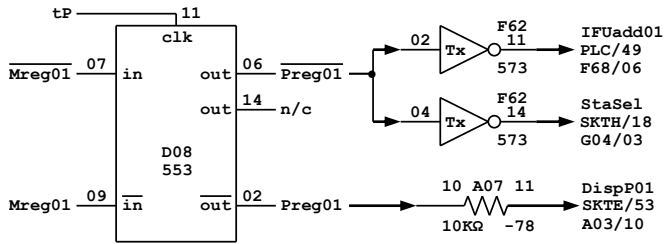
920M RESET & TIMING

Terry Froggatt, October 2023

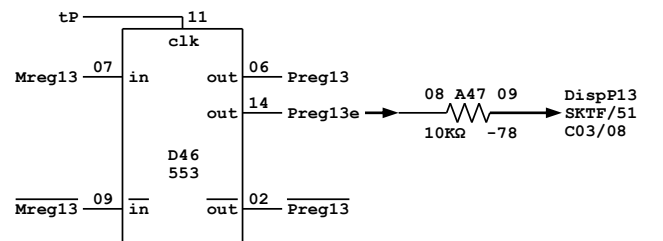
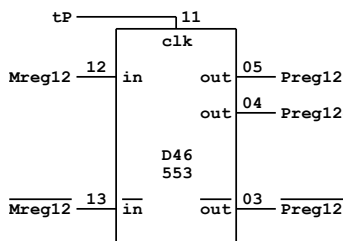
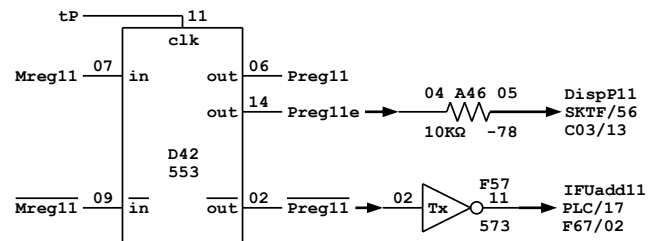
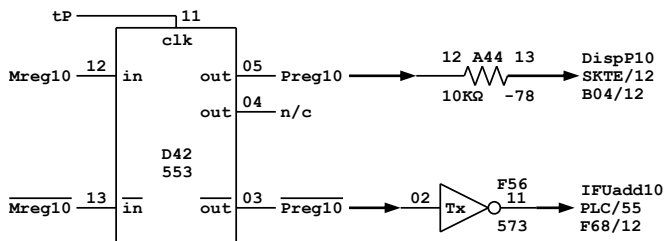


920M PERIPHERAL ADDRESS REGISTER

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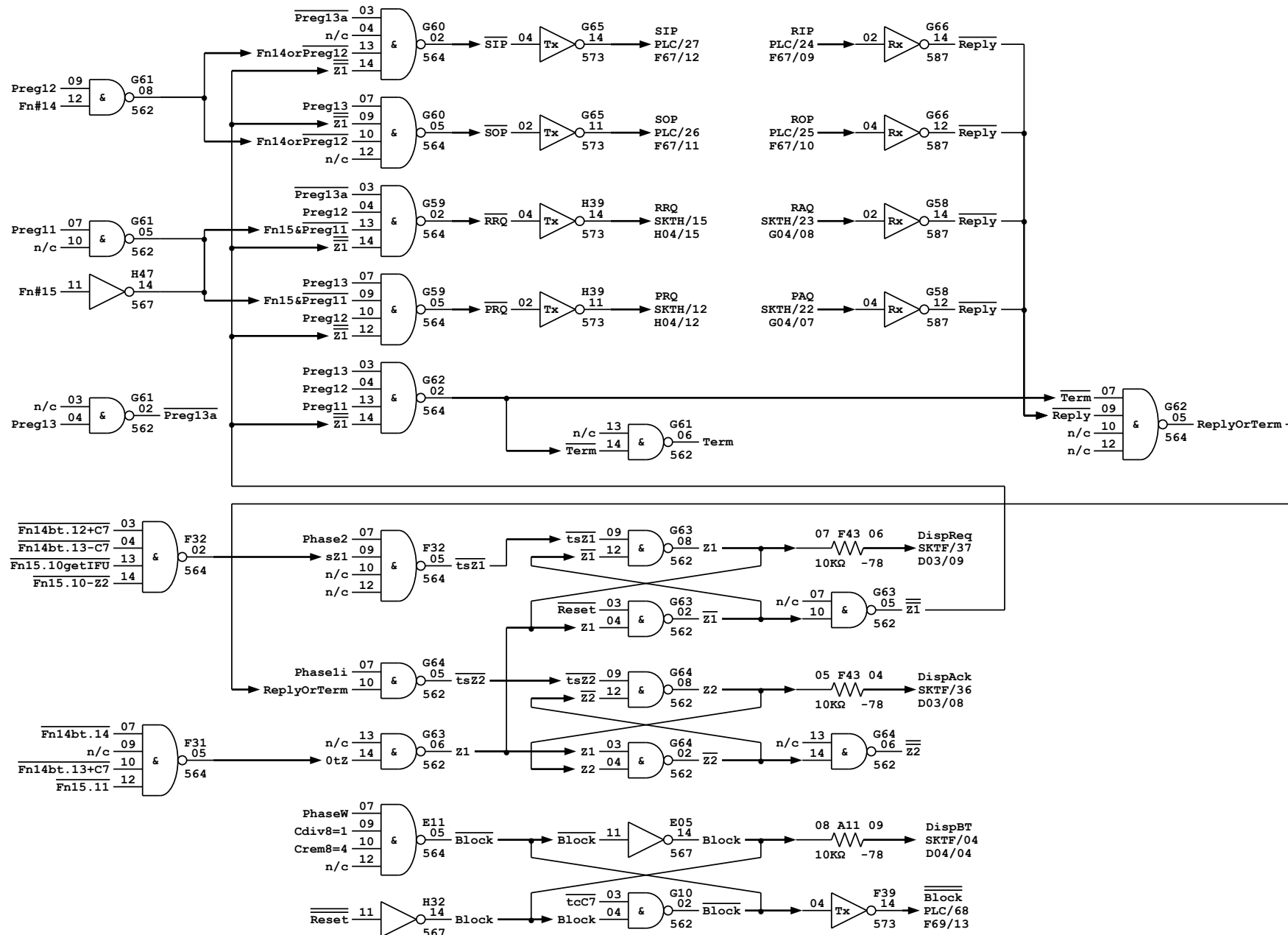


Preg11 Preg12 Preg13 go to I/O request routing



Terry Froggatt, October 2023

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920M INTERRUPT LOGIC

Terry Froggatt, October 2023

